

Fig. 1

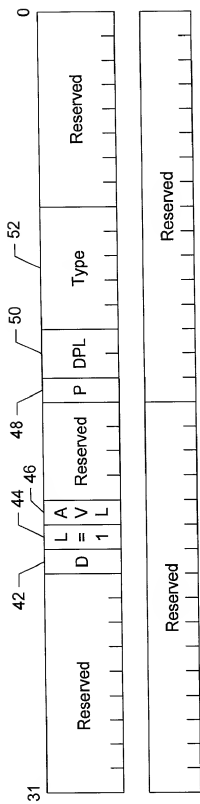


Fig. 2

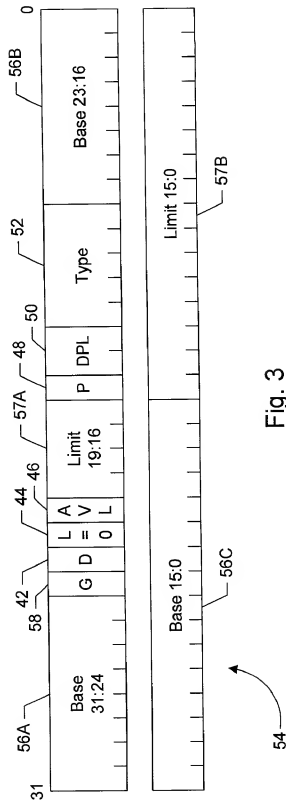


Fig. 3

<u>LMA</u>	<u>CS L Bit</u>	<u>CS D Bit</u>	<u>Operating Mode</u>
0	x	0	16 Bit Mode
0	x	1	32 Bit Mode
1	0	0	16 Bit Compatibility Mode
1	0	1	32 Bit Compatibility Mode
1	1	0	32/64 Mode
1	1	1	Reserved

70

Fig. 4

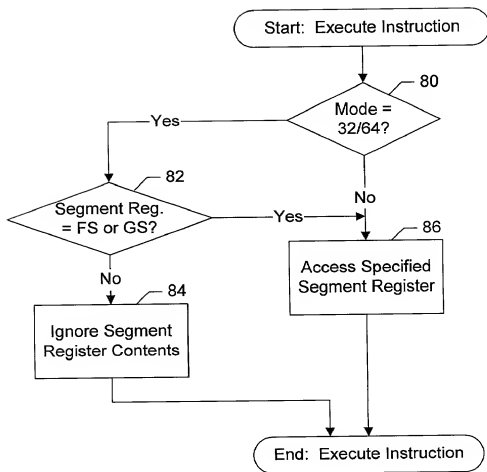


Fig. 5

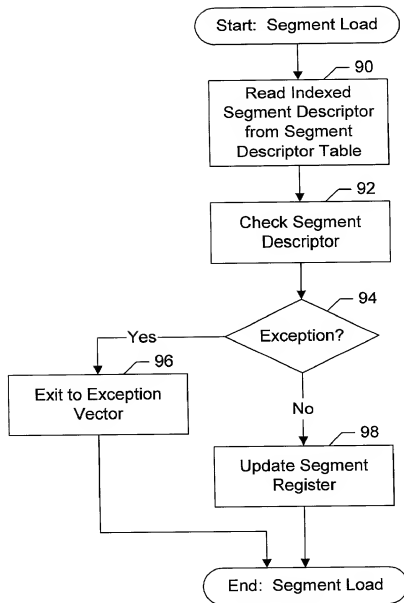


Fig. 6

<u>Segment Regs Affected</u>	<u>Check</u>
CS	Segment Descriptor Type = Code Segment
SS	Segment = Writeable Data Segment
SS	Privilege Level of Segment Matches Current Privilege Level
SS	Segment is Not Null Segment
DS, ES, FS, GS	Segment = Data or Readable Code Segment
DS, ES, FS, GS	Privilege Level of Segment is Less than or Equal to Current Privilege Level
All	Selector Index is Within Descriptor Table Limit
All	Segment is Present

100

Fig. 7

O/S -- Mode = 32/64, Segment  
Register Contents Ignored

Application -- Mode = 32 Bit  
Compatibility, Segment Register  
Contents Used

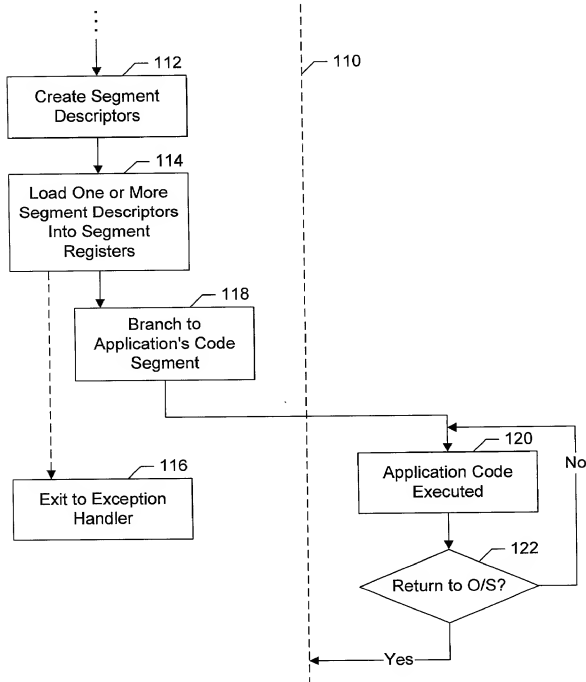


Fig. 8

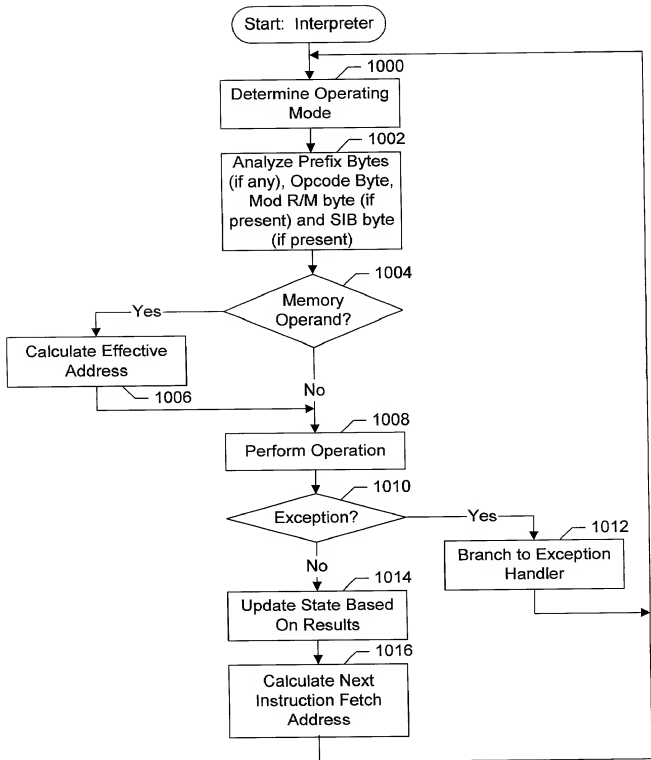


Fig. 9



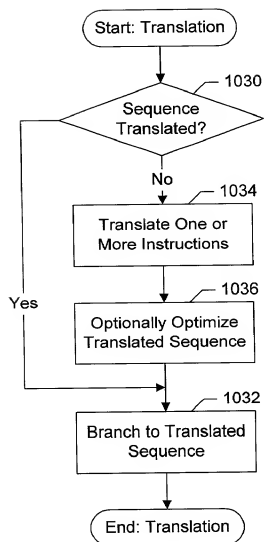


Fig. 10

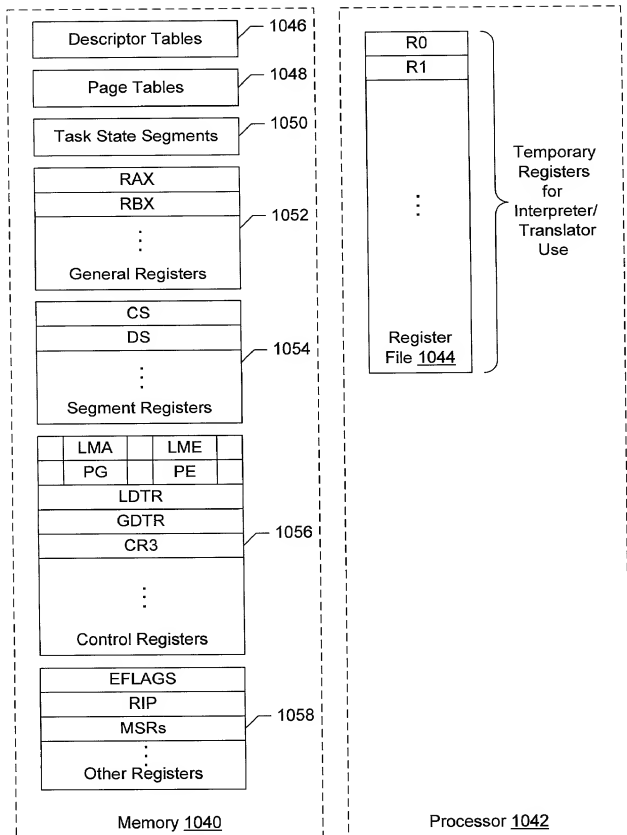


Fig. 11

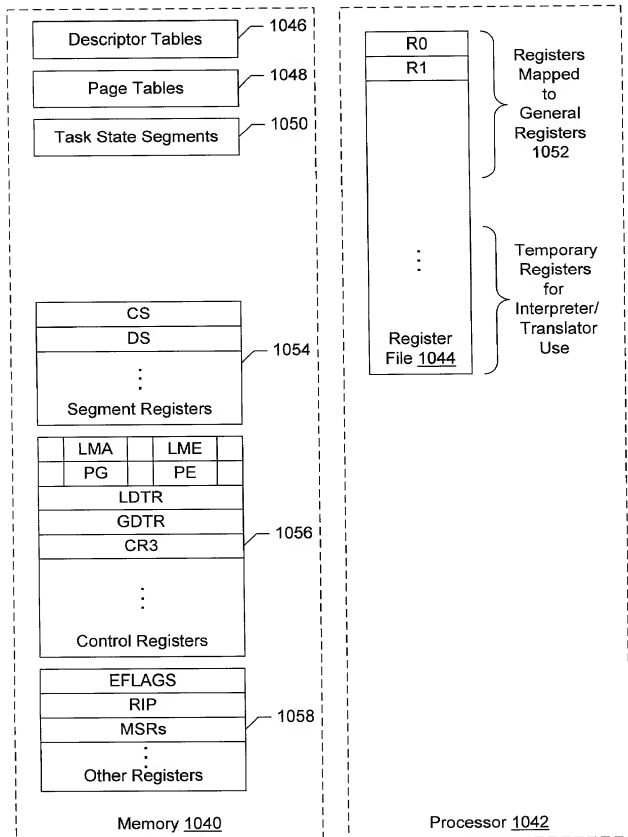


Fig. 12

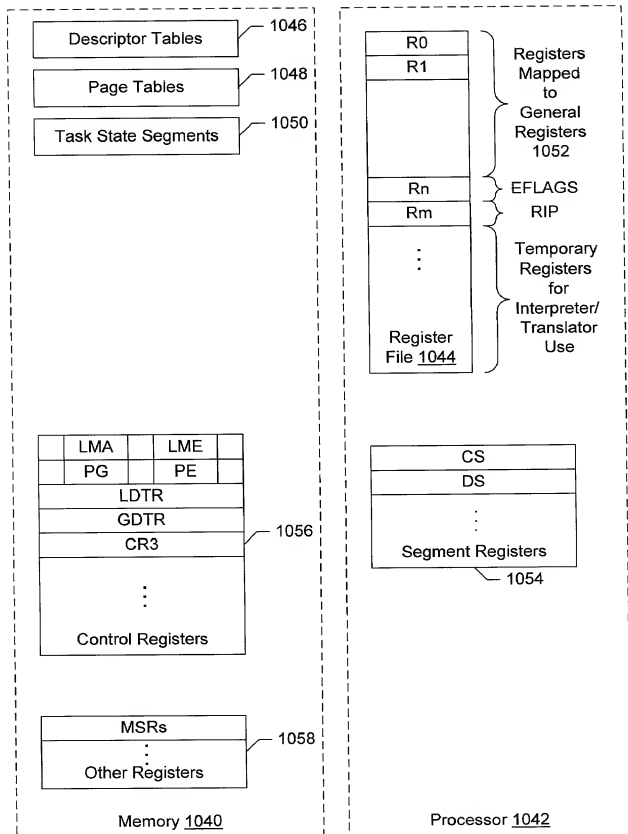


Fig. 13

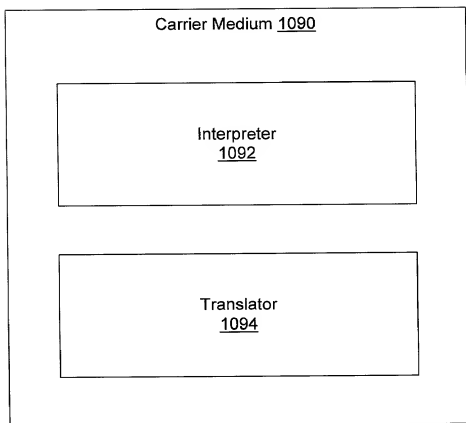


Fig. 14

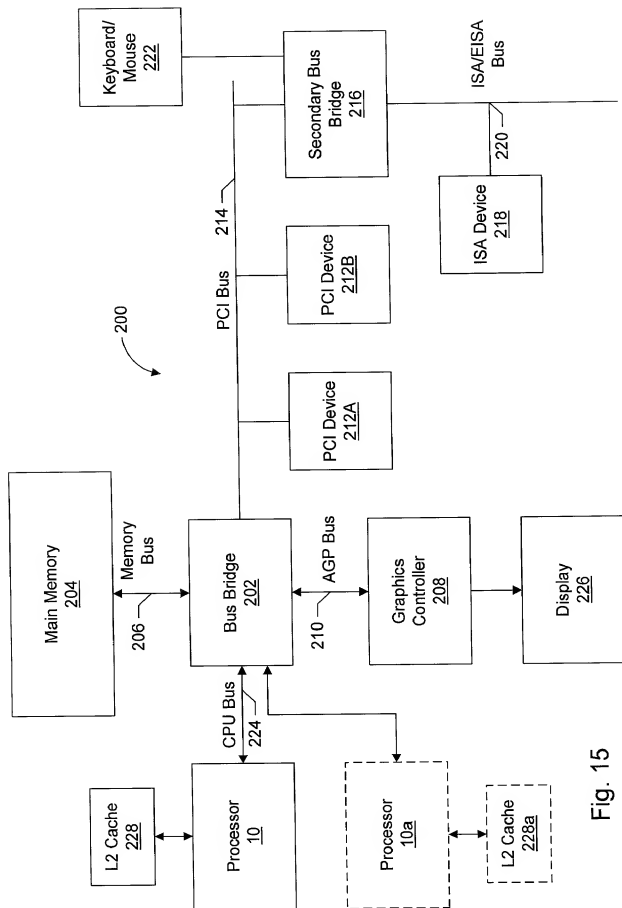


Fig. 15

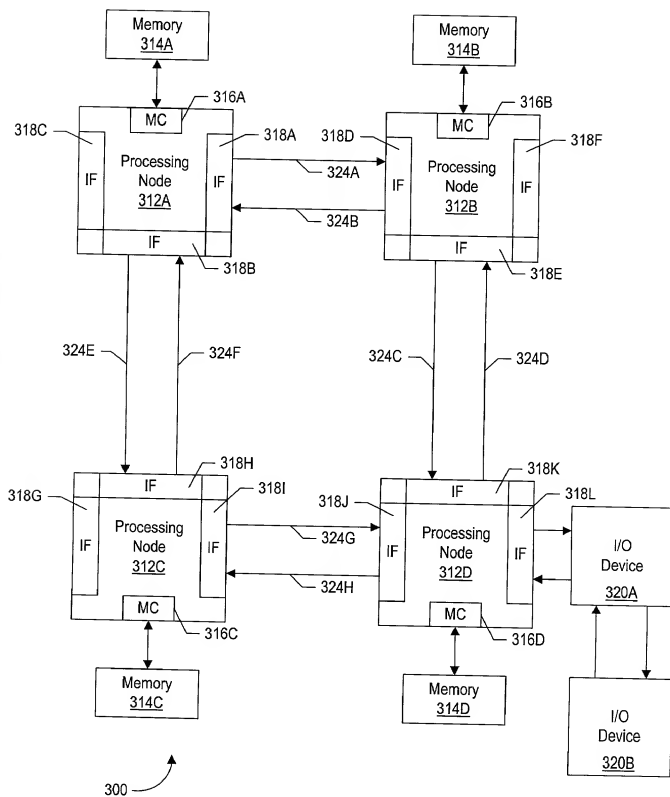


Fig. 16